



Patent

Docket No.: CYPR-CD01168M

Information Disclosure Statement Transmittal

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.			
Date of Deposit:	10/12/04	Name of Person Making the Deposit:	KATHERINE RINALDI
		Signature of the Person Making the Deposit:	<i>Katherine Rinaldi</i>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Manfred Bartz, Marat Zhaksilikov, Steve Roe, Kenneth Y. Ogami, Matthew A. Pleis and Douglas H. Anderson

Application No.: 09/989,571

Group Art Unit:

Filed: 11/19/01

Examiner:

Title: METHOD FOR FACILITATING MICROCONTROLLER PROGRAMMING

Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450
Sir:

Information Disclosure Statement Transmittal

Transmitted herewith is the following:

- Formal drawings, totaling sheets.
..... Informal drawings, totaling sheets.
..... Certification for PTO Consideration
..... Information Disclosure statement (___ sheets)
☒ Information Disclosure statement and late filing fee
☒ Form 1449
..... Petition for Extension of Time
☒ Other:

Fee Calculation (for other than a small entity)

Fee Items	Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)	\$.00	\$0.00
Information Disclosure Statement, late filing	\$180.00	\$180.00
Other:		\$0.00
Total Fees		\$180.00

PAYMENT OF FEES


- The full fee due in connection with this communication is provided as follows:
 - ☒ The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
 - ☒ A check in the amount of \$180.00
 - ☐ Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
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Respectfully submitted,

Date: 10/12/04

By: 
Ronald M. Pomeranke
Reg. No. 43,009



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: CYPR-CD01168M

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Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

<u>Pat. No.</u>	<u>Pat. Title</u>	<u>Grant Date</u>
6,715,132	DATASHEET BROWSING AND CREATION WITH DATA -DRIVEN DATASHEET TABS WITHIN A MICORCONTROLLER DESIGN TOOL	03/30/04
6,014,135	COLLABORATION CENTRIC DOCUMENT PROCESSING ENVIRONMENT USING AN INFORMATION CENTRIC VISUAL USER INTERFACE AND INFORMATION PRESENTATION METHOD	01/11/00
4,813,013	SCHEMATIC DIAGRAM GENERATING SYSTEM USING LIBRARY OF GENERAL PURPOSE INTERACTIVELY SELECTABLE GRAPHIC PRIMITIVES TO CREATE SPECIAL APPLICATION ICONS	03/14/89
6,121,965	USER INTERFACE FOR GRAPHICAL APPLICATION TOOL	09/19/00
6,578,174	METHOD AND SYSTEM FOR CHIP DESIGN USING REMOTELY LOCATED RESOURCES	06/10/03
5,225,991	OPTIMIZED AUTOMATED MACRO EMBEDDING FOR STANDARD CELL BLOCKS	07/06/93
6,460,172	MICROPROCESSOR BASED MIXED SIGNAL FIELD PROGRAMMABLE INTERGRATED DEVICE AND PRTOTOTYPING METHODOLOGY	10/01/02

10/20/2004 AWONDAF1 00000066 09989571

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The Examiner's attention is respectfully directed to the following U.S. Published Patent Applications:

<u>Pub. No.</u>	<u>Pub. Title</u>	<u>Pub. Date</u>
2002/0188910	METHOD AND SYSTEM FOR CHIP DESIGN USING REMOTELY LOCATED RESOURCES	12/12/02
2002/0099863	SOFTWARE SUPPORT LAYER FOR PROCESSORS EXECUTING INTERPRETED LANGAUAGE APPLICATIONS	07/25/02
2002/0170050	METHODS AND APPARATUS FOR UPGRADING FIRMAWARE IN AN EMBEDDED SYSTEMS	11/14/02
2002/0144099	HARDWARE ARCHITECTRE FOR FAST SERVICING OF PROCESSOR INTERRUPTS	10/03/02
2002/0183956	TESTING COMPLIANCE OF A DEVICE WITH ABUS PROTOCOL	12/05/02
2002/0156929	XML-BASED SYSTEM AND METHOD FOR COLLABORATIVE WEB-BASED DESIGN AND VERIFICATION OF A SYSTEM-ON-A-CHIP	10/24/02

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Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	6,715,132	03/30/04	Bartz et al.	716	1	11/19/01
	B	6,014,135	01/11/00	Fernandes	345	331	04/04/97
	C	4,813,013	03/14/89	Dunn	364	900	12/22/86
	D	6,121,965	09/19/00	Kenney et al.	345	339	10/17/97
	E	6,578,174	06/10/03	Zizzo	716	1	06/08/01
	F	5,225,991	07/06/93	Dougherty	364	491	04/11/91
	G	6,460,172	10/01/02	Insenser Farre et al.	716	17	06/21/00

U.S. Published Patent Applications

Examiner Initial	No.	Pub. No.	Pub. Date	Applicant	Class	Sub-class	Filing Date
	H	2002/0188910	12/12/02	Zizzo	716	1	06/08/01
	I	2002/0099863	07/25/02	Comeau et al.	709	310	01/22/01
	J	2002/0170050	11/14/02	Fiorella, III et al.	717	168	03/14/01
	K	2002/0144099	10/03/02	Muro, Jr. et al.	712	228	01/25/01
	L	2002/0183956	12/05/02	Nightingale	702	120	02/28/02
	M	2002/0156929	10/24/02	Hekmatpour	709	310	04/23/01

Foreign Patent or Published Foreign Patent Applications

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	N							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	O	
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.